**TIME TABLE FOR 2nd SEM. ELECTRONICS & COMMUNICATION ENGINEERING FOR THE SESSION 2023-24**

 **W.E.F: 05/02/2024**

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **TIME****DAY** | **9:00 AM-10:00 AM** | **10:00 AM-11:00 AM** | **11:00 AM-12:00 PM** | **12:00 -****1:00 PM** | **1:00 PM-2:00 PM** | **2:00 PM-****3:00 PM** | **3:00 PM-****4:00 PM** | **4:00 PM-****5:00 PM** |
| **MONDAY** |  | AUDIT(C-104) | ITCT(SKS) | OE 1(A1-307) | **BREAK** |  |  |  |
| **TUESDAY** | OE 1(A1-307) | OC(AT) | AC LAB-II (AT/SP) (Communication Lab) | ITCT(SKS) |  |  |
| **WEDNESDAY** | AUDIT(C-104) | ITCT(SKS) | ADSP(JS) | OE 1(A1-307) |  | Project |
| **THURSDAY** | AC LAB-II (JS/SP) (Communication Lab) | OC(AT) | OC(AT) | ARS(NCN) | ADSP(JS) |  |
| **FRIDAY** | ARS(NCN) | ARS(NCN) | ADSP(JS) |  |  | Project |
| **SATURDAY** | REMEDIAL CLASSES, STUDENT MENTORING, INDUSTRIAL VISITS ETC. ON 1ST AND 3RD SATURDAYS OF EVERY MONTH |

**Room No: C 101 (Smart Class Room)**

**OC: Optical Communication, AT: ARUNA TRIPATHY**

**ITC: Information Theory & Coding, SKS: SUSHANT KUMAR SAHU**

**ADSP: Adv. Digital Signal Processing, JS: JAGANATH SETHI**

**ARS: Antenna & Radiating System, NCN: NARESH CHANDRA NAIK**

**SP: SAGUPHA PARWEEN,**

**OE-1: OR (Operational Research, Math dept )**

**AUDIT: DISASTER MANAGEMENT (CIVIL DEPT)**



 **PIC, TIME TABLE**

**TIME TABLE FOR 2nd SEM. VLSI & EMBEDDED SYSTEM FOR THE SESSION 2023-24**

**W.E.F: 05/02/2024**

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| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **TIME****DAY** | **9:00 AM-10:00 AM** | **10:00 AM-11:00 AM** | **11:00 AM-12:00 PM** | **12:00 -****1:00 PM** | **1:00 PM-2:00 PM** | **2:00 PM-****3:00 PM** | **3:00 PM-****4:00 PM** | **4:00 PM-****5:00 PM** |
| **MONDAY** |  | AUDIT(C-104) | MSHDL(AB) | ASD(SD) | **BREAK** |  |  |  |
| **TUESDAY** |  | LPVS(TKP) | ASD(SD) | NST(PHYSICS) |  | ES & HDL LAB (TKP/SKS)(VLSI LAB) |
| **WEDNESDAY** | AUDIT(C-104) | NST(PHYSICS) | ESD(AD) | MSHDL(AB) |  | Project |
| **THURSDAY** |  | ESD(AD) | MSHDL(AB) | LPVS(TKP) |  | ES & HDL LAB (AB/SKS)(VLSI LAB) |
| **FRIDAY** | NST(PHYSICS) | ASD(SD) | LPVS(TKP) | ESD(AD) |  | Project |
| **SATURDAY** | REMEDIAL CLASSES, STUDENT MENTORING, INDUSTRIAL VISITS ETC. ON 1ST AND 3RD SATURDAYS OF EVERY MONTH |

**Room No-C-107**

**LPVS: Low Power VLSI System, TKP: TAPAS KUMAR PATRA**

**ASD: ASIC and SoC Design, SD: SATYABHAMA DASH**

**MSHDL: Modeling and Synthesis using HDL, AB: ABHYARTHANA BISOYI**

**ESD: Embedded System Design, AD: ANANYA DASTIDAR**

**Audit: Disaster Management, Civil Dept**

**NST: Nano-Science & Technology, Physics Dept**

**ES & HDL Lab: Embedded System & HDL lab**



 **PIC, TIME TABLE**